

Patented 06-01-2003

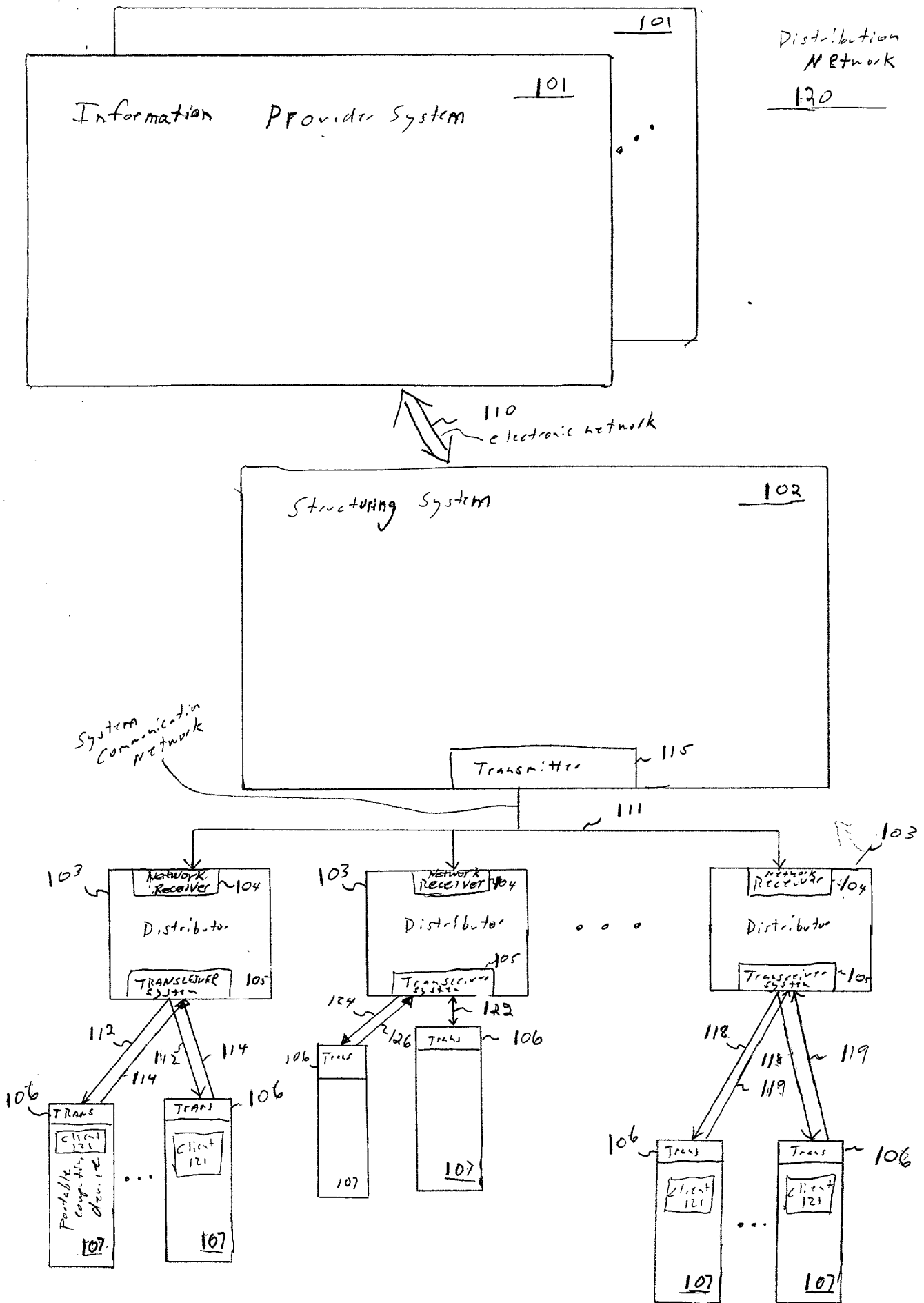


FIG. 1

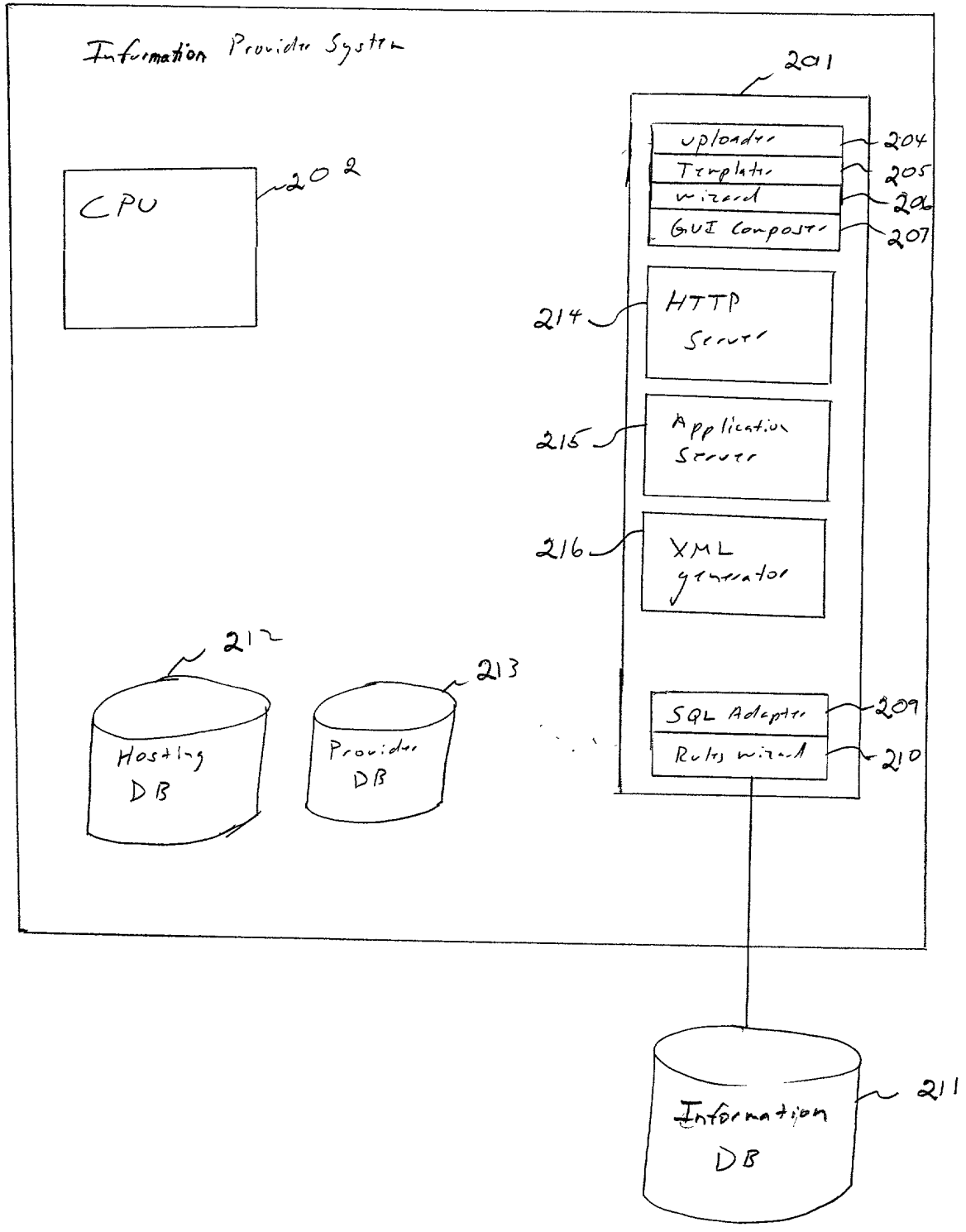
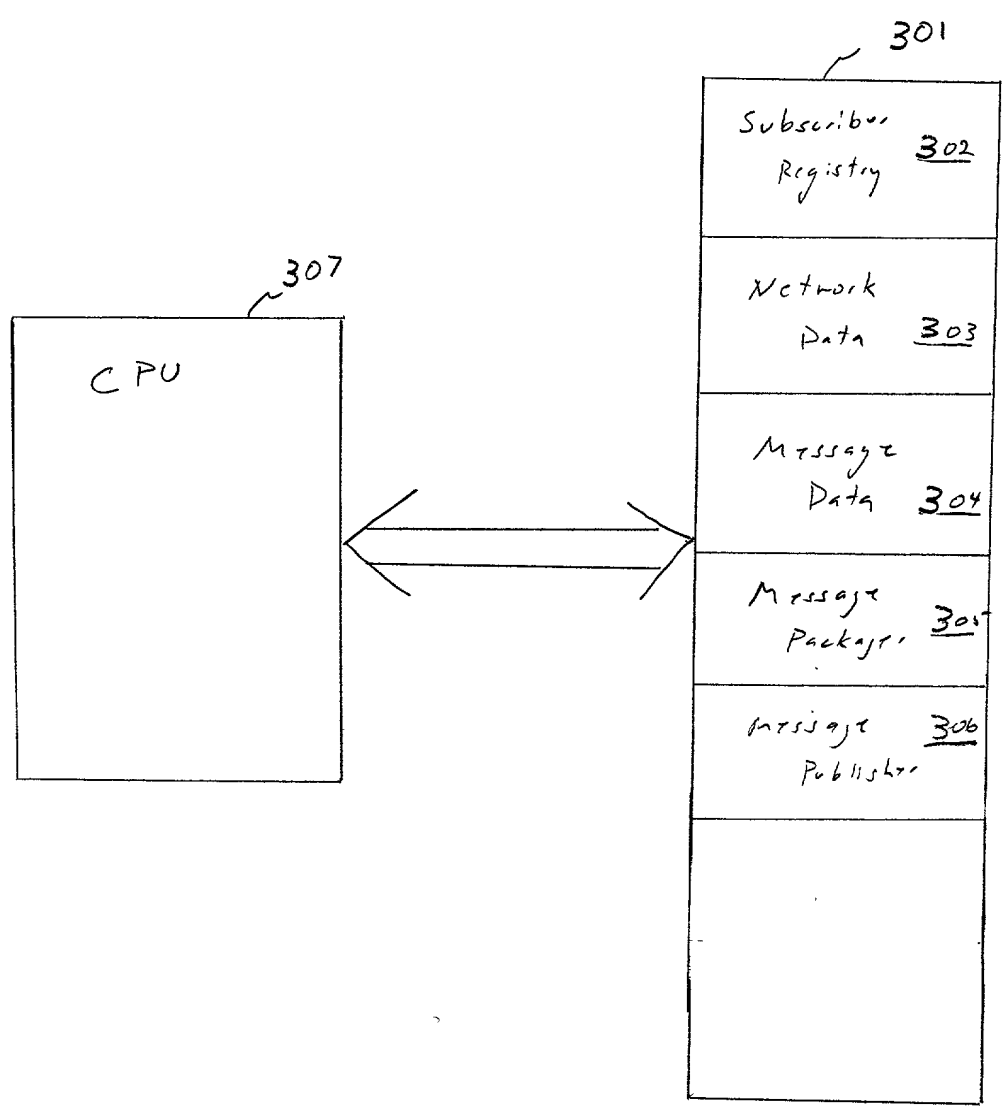


Fig. 2

Structuring System



TRANSMITTER 115

III

FIG. 3

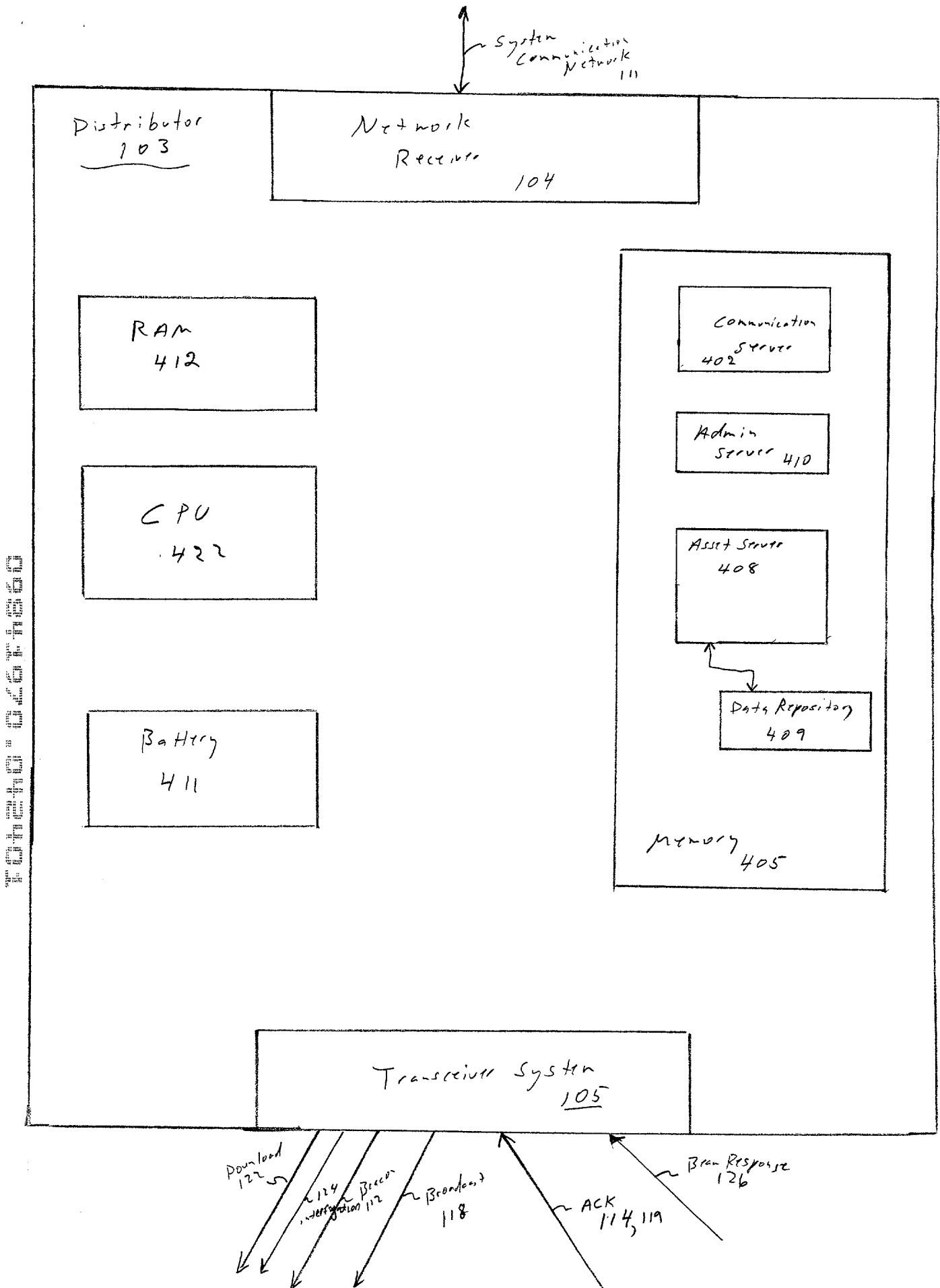
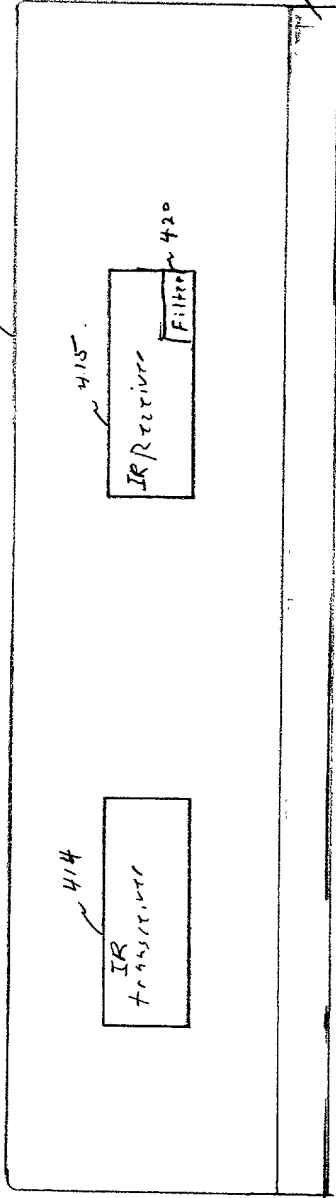


FIG. 4

FIG. 4A

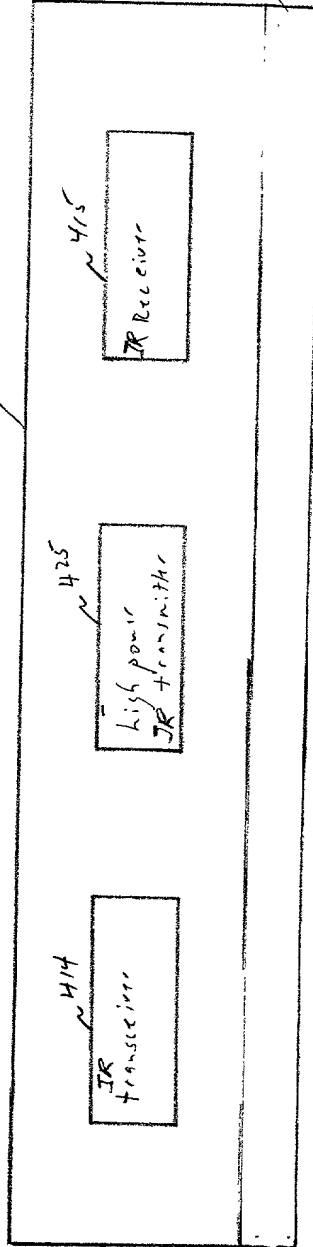
transceiver system
105



IR window
421

FIG. 4A

transceiver system
105



IR window
421

FIG. 4B

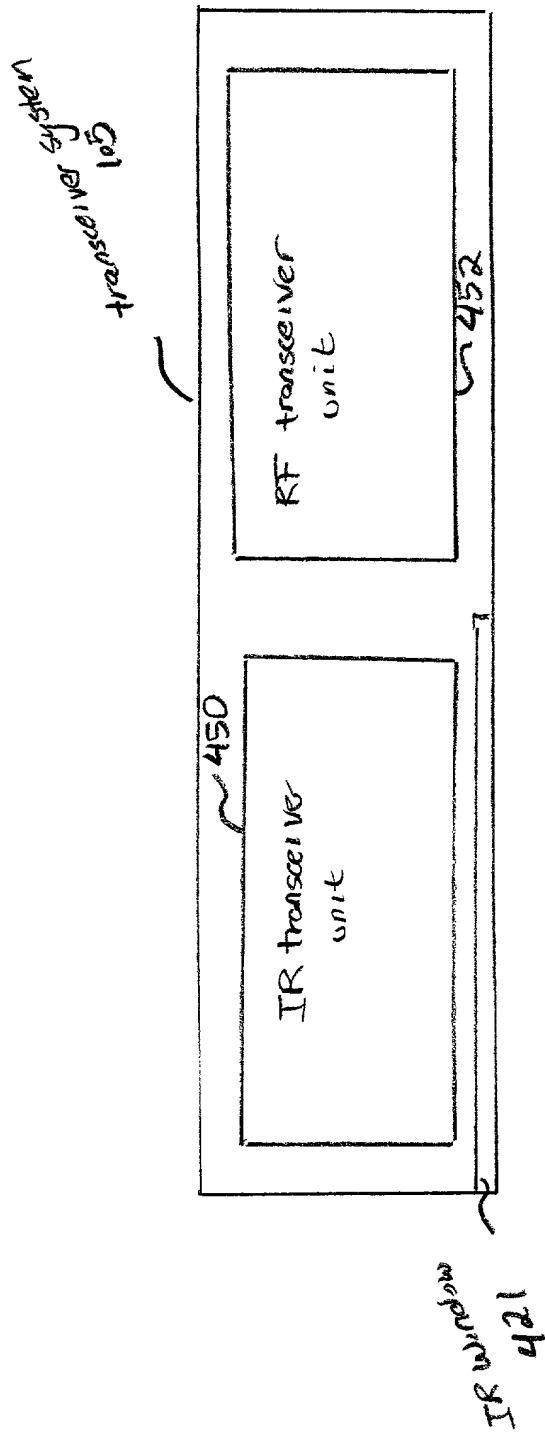


Fig. 4C

APPLICATION LAYER

COM
SERVER
402

ASSET
SERVER
408

DB
409

ADMIN
SERVER
410

PLATFORM LAYER

Cross
platform
interface

Manager

Platform
independent
interface

H/W op. access
Status Indicator Interface
Platform Feature Set

Abstract
endpoint
interface

test
function
suite

SYSTEM LAYER

API

H/W
drivers

Kernel
RTOS

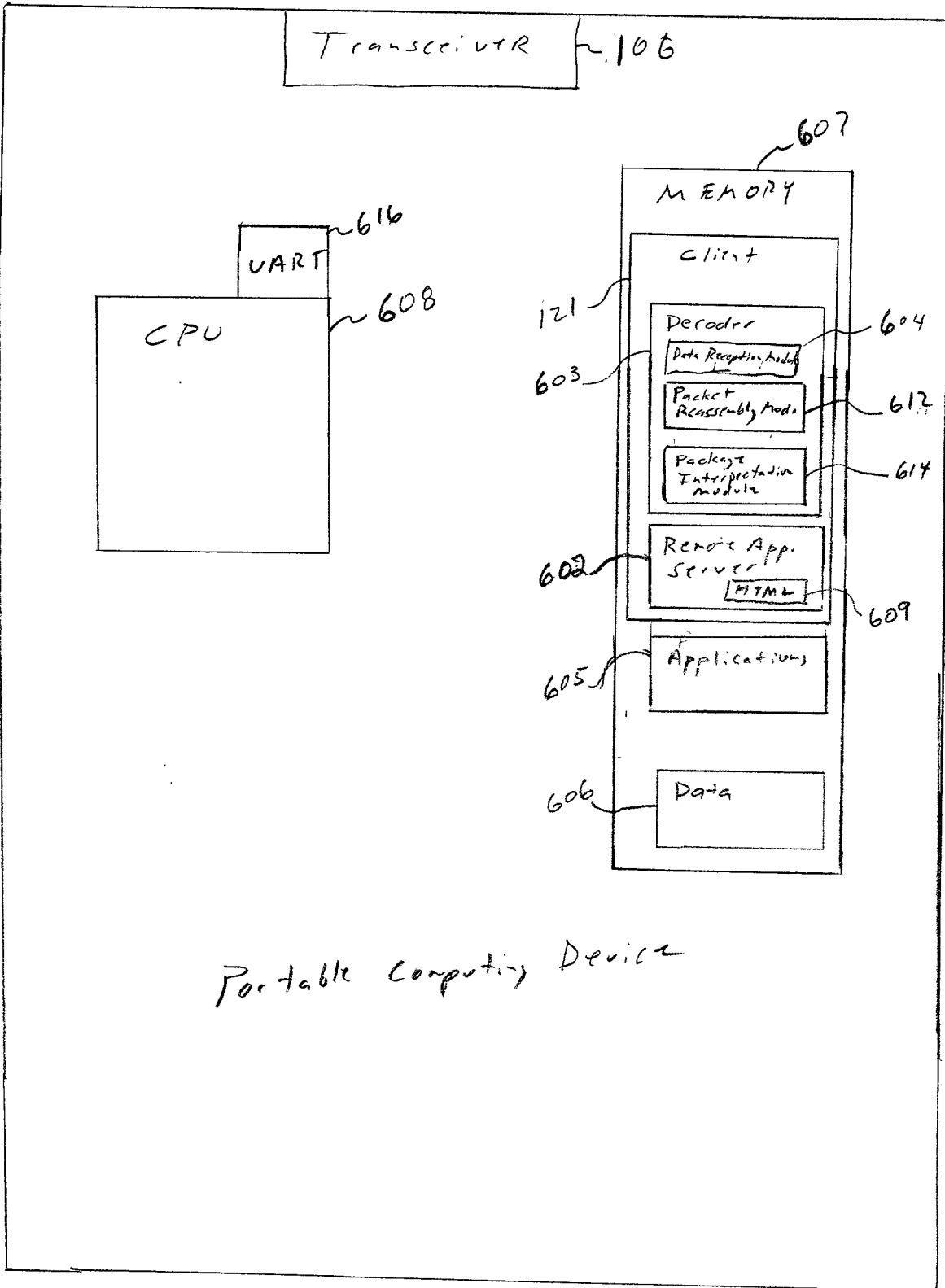
Boot Loader
520

Transceiver
Protocol
Stacks

Network
Receiver
Comm
Stacks

test
function

Fig. 5



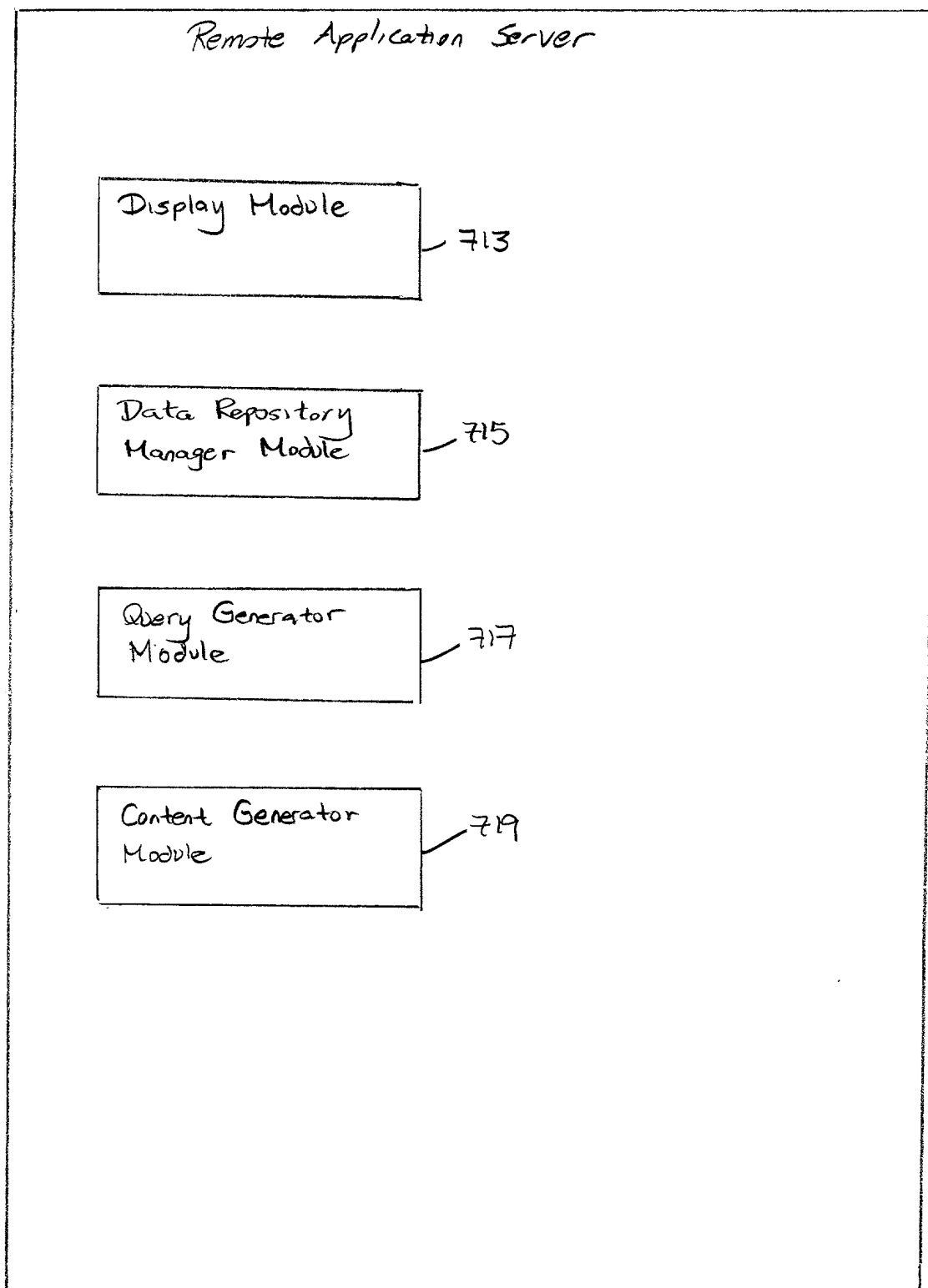


Fig. 7A

725 /

Receive Information Including Logic Portion,
Data Repository, and Formatting Information



727 /

Produce a Search Query in Accordance
with user input and in Accordance with Logic
Portion



729 /

Search Data Repository in Accordance
with Search Query to Retrieve Search Result



731 /

Generate Content in Accordance with
Search Result and in Accordance with
Formatting Information



733 /

Display Content to user of portable
computing device

602

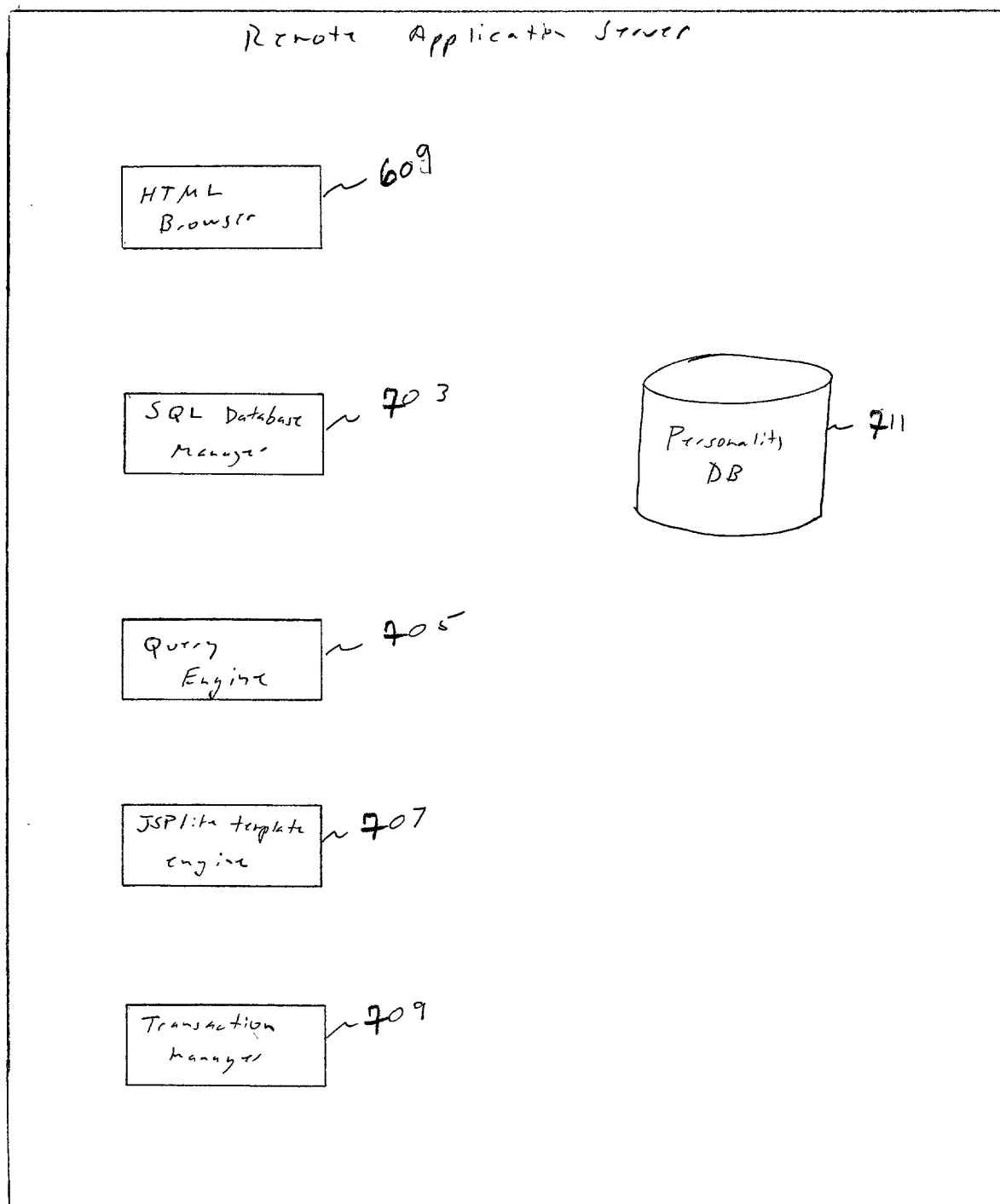


Fig. 7C

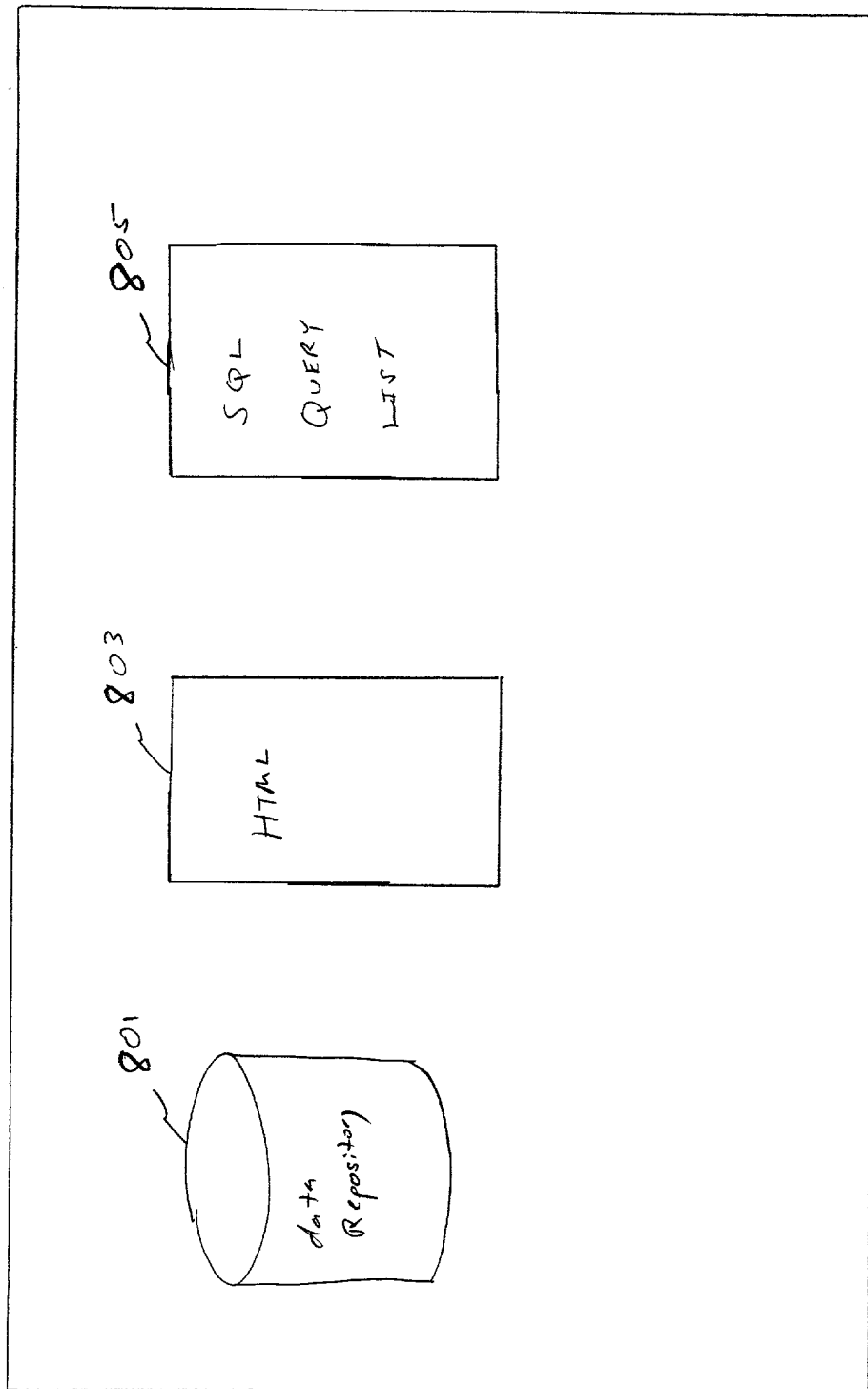


FIG. 8

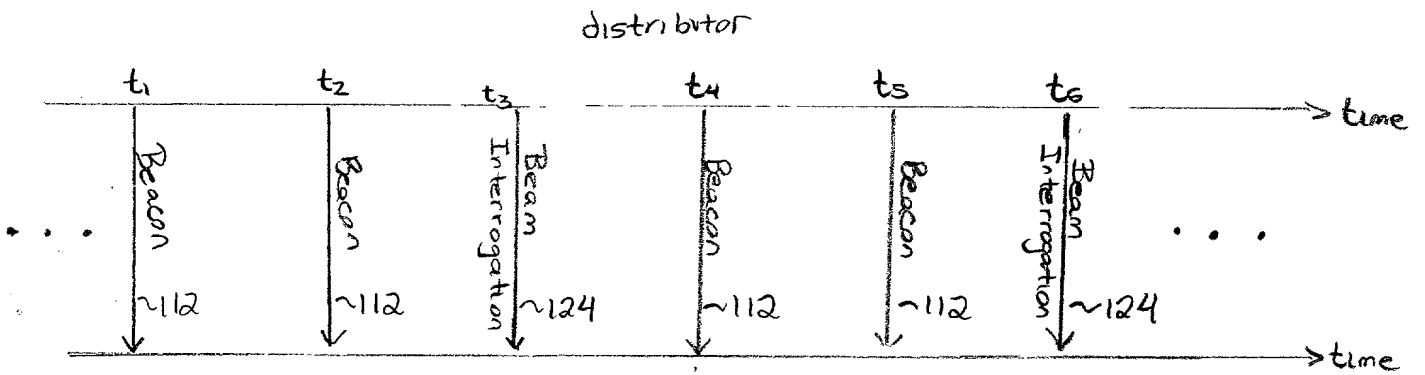


Fig. 9

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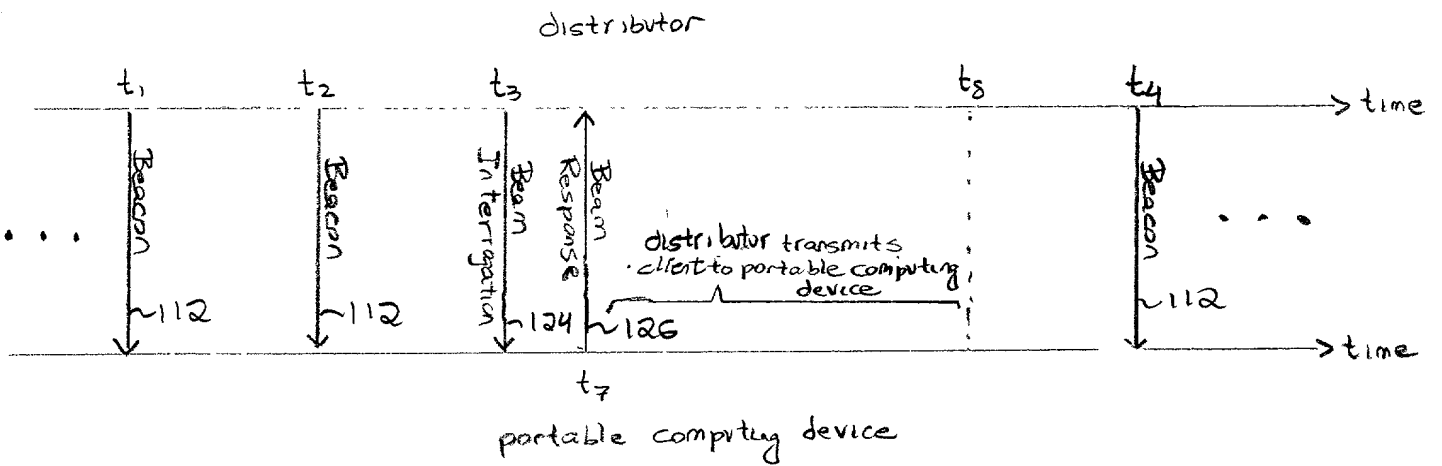
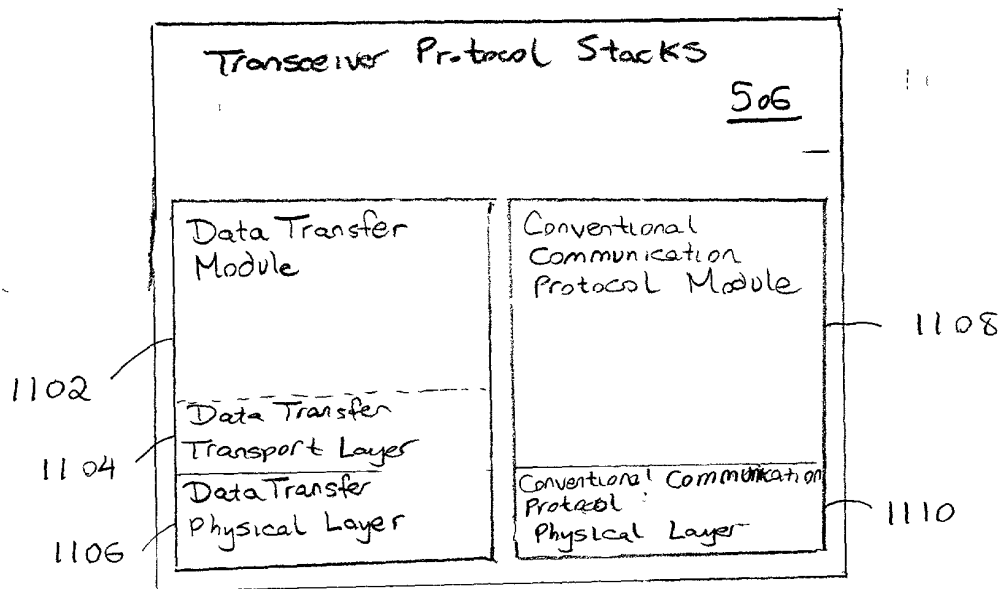
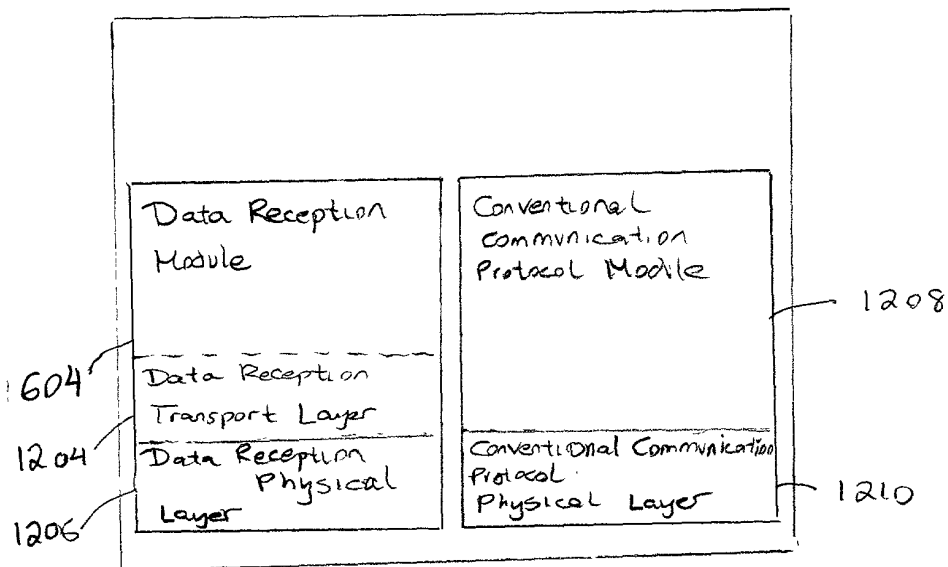


Fig. 10



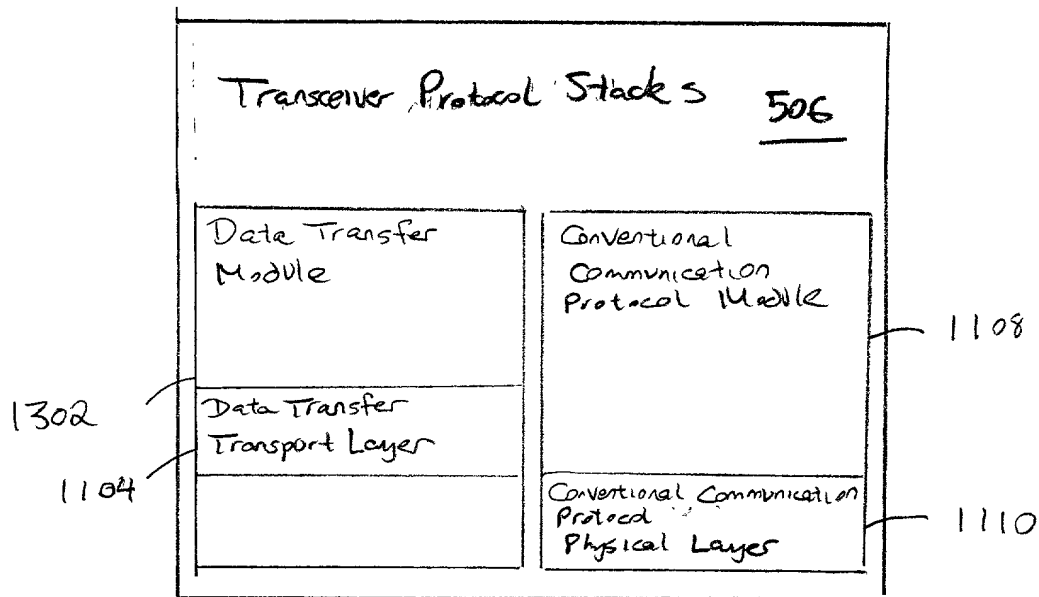
Distributor

Fig. 11



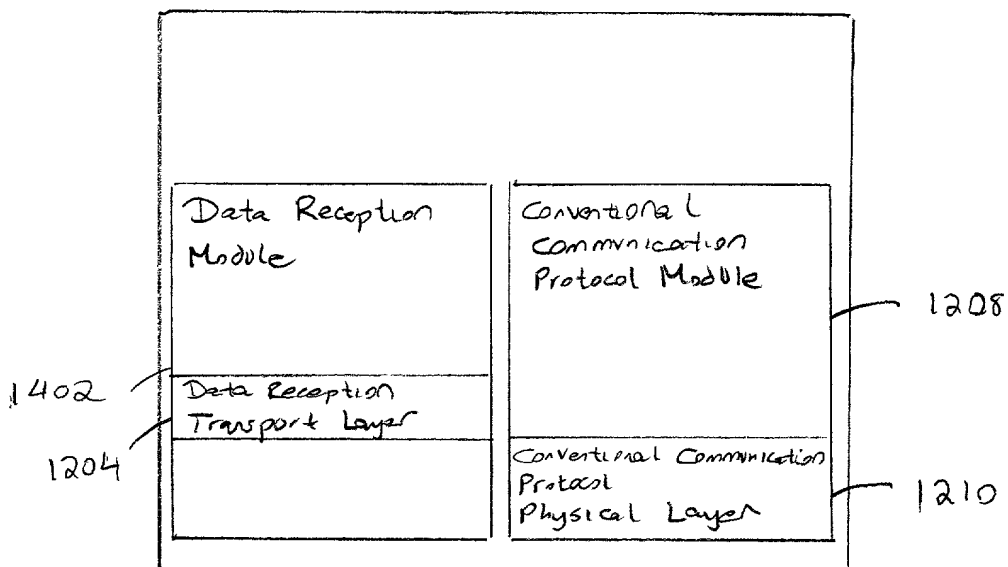
Portable Computing Device

Fig. 12



Distributor

Fig. 13



Portable Computing Device

Fig. 14

1500

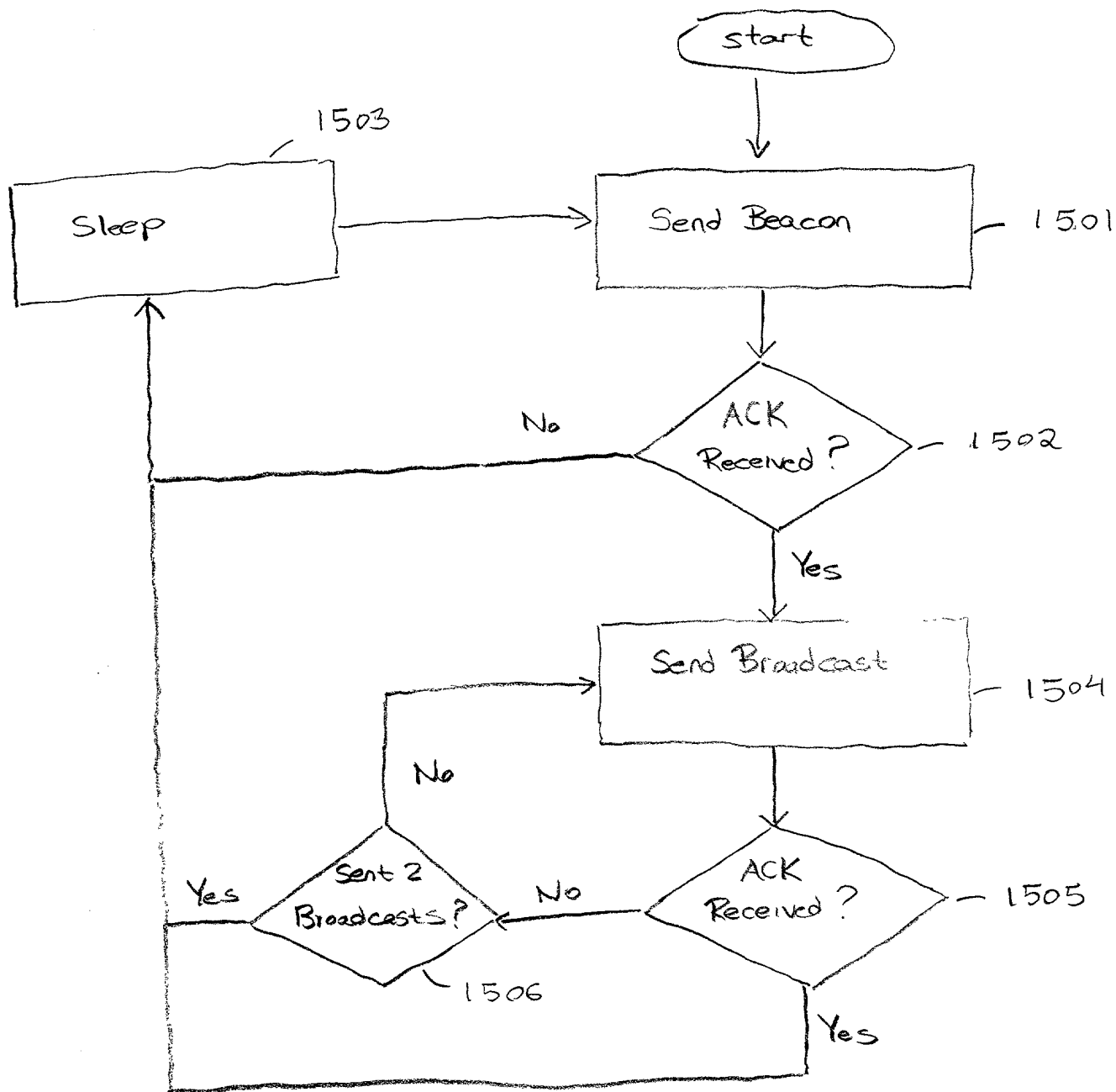


Fig. 15

No ACK Received After Sending Beacon

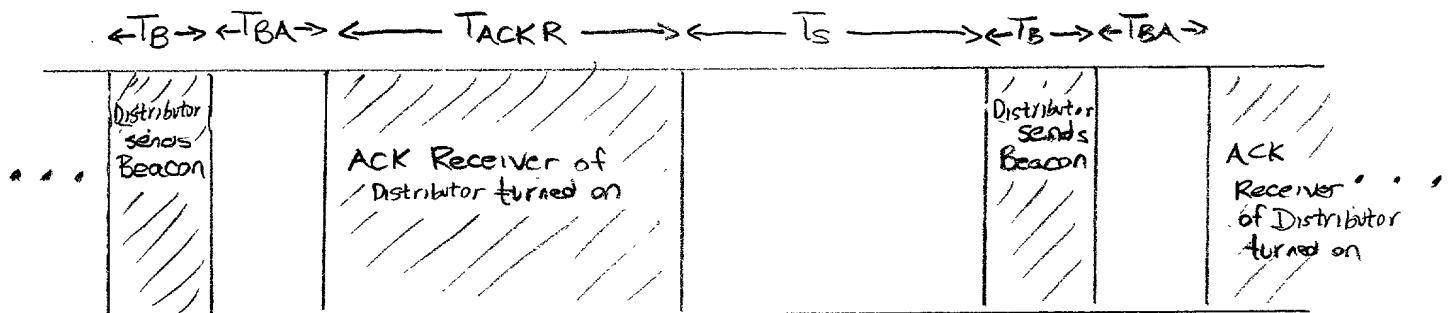


Fig. 16

ACK Received After Sending Beacon

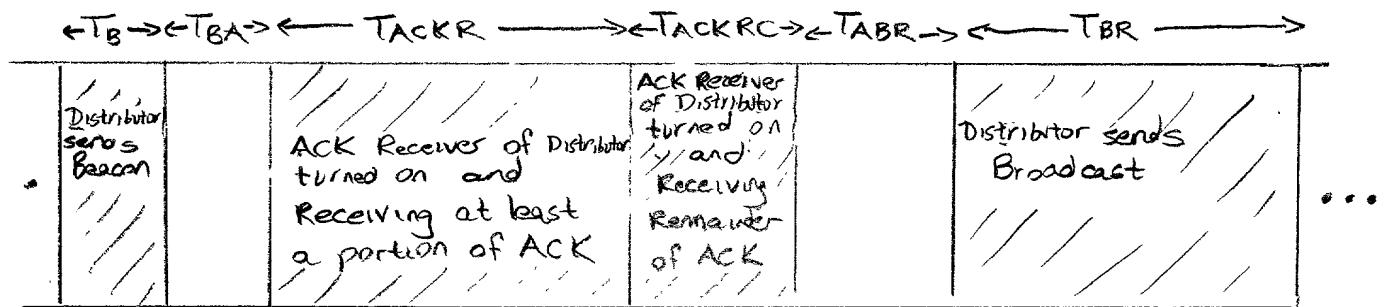


Fig. 17

No ACK Received After Sending Broadcast

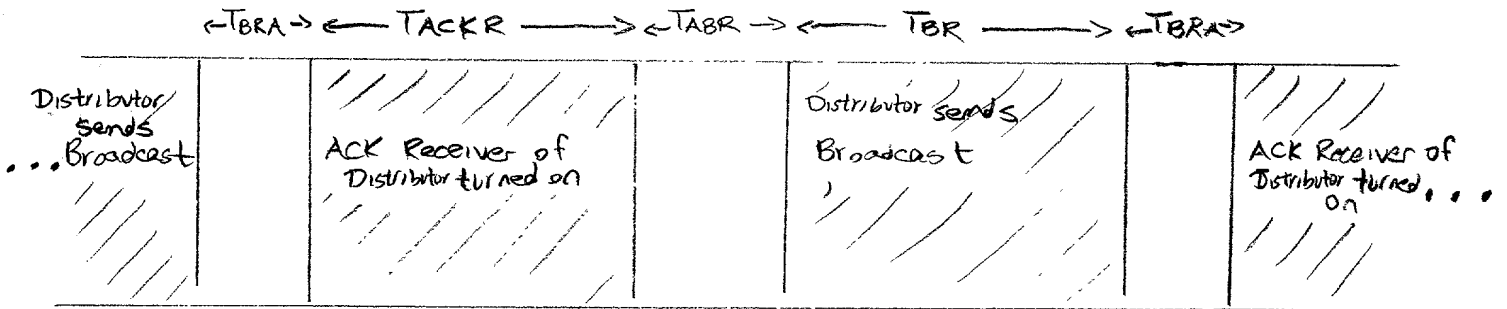


Fig. 18

ACK Received After Sending Broadcast

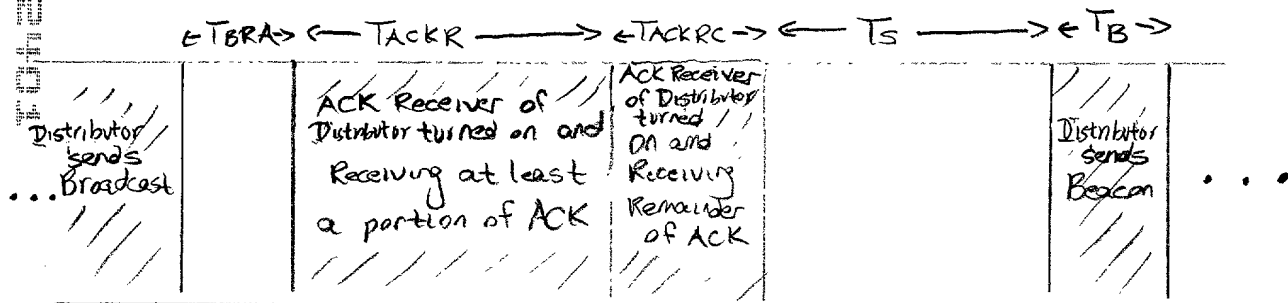


Fig. 19

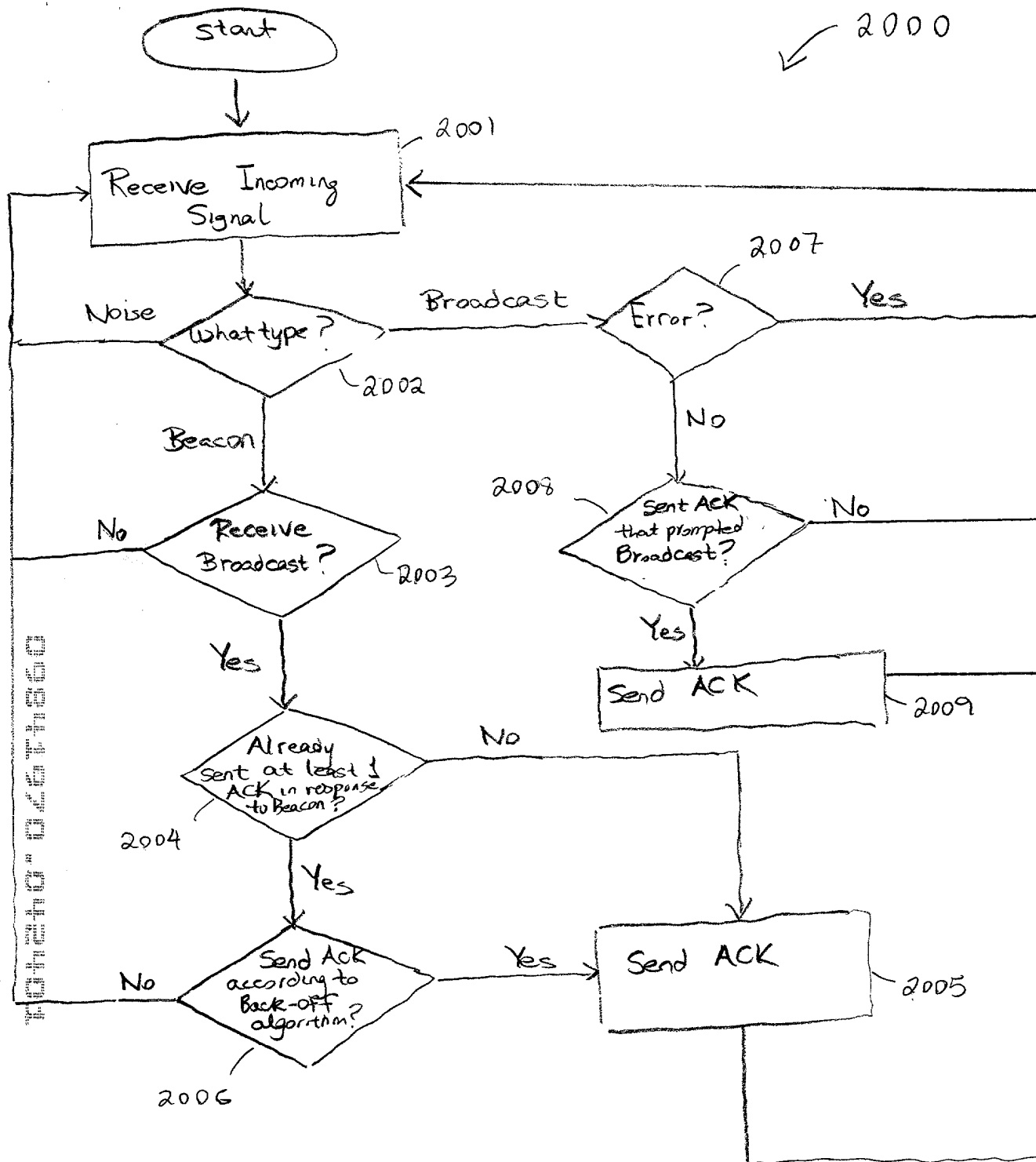


Fig. 20

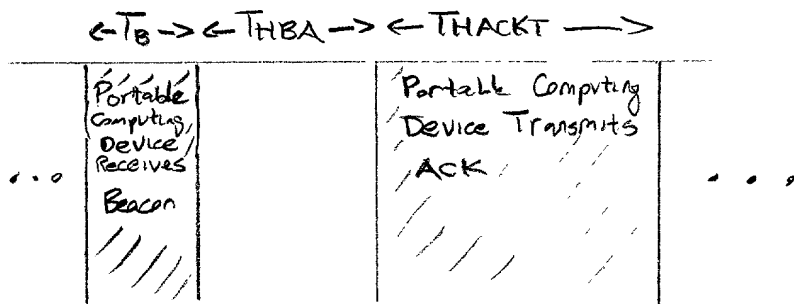


Fig. 21

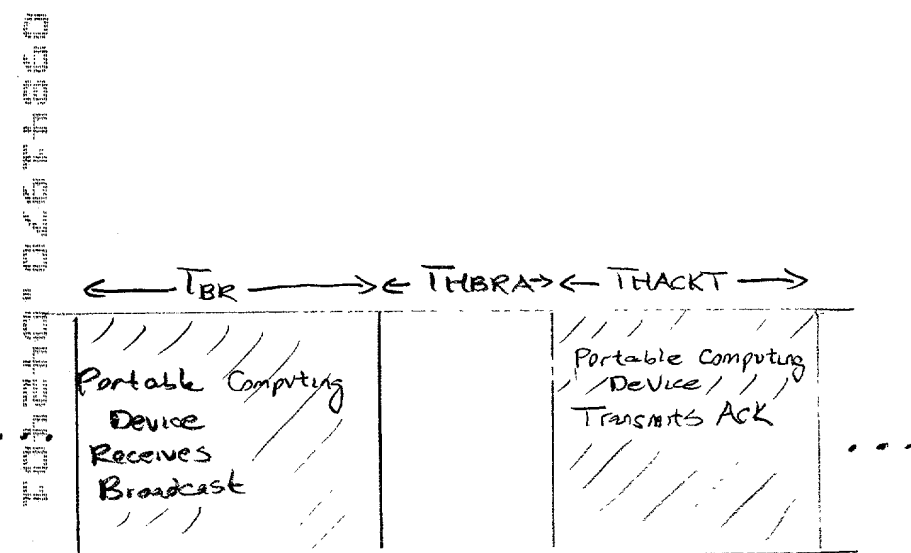


Fig. 22

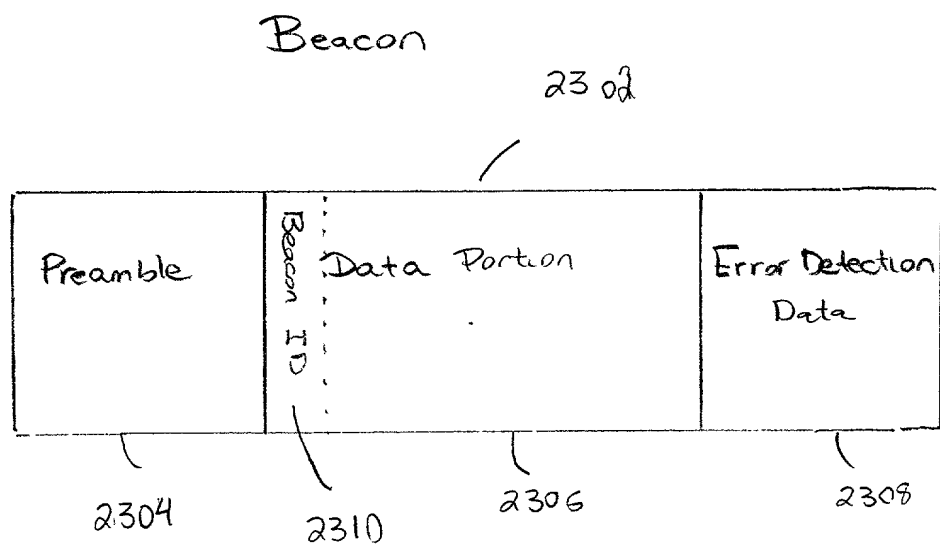


Fig. 23

2025 RELEASE UNDER E.O. 14176

ACK

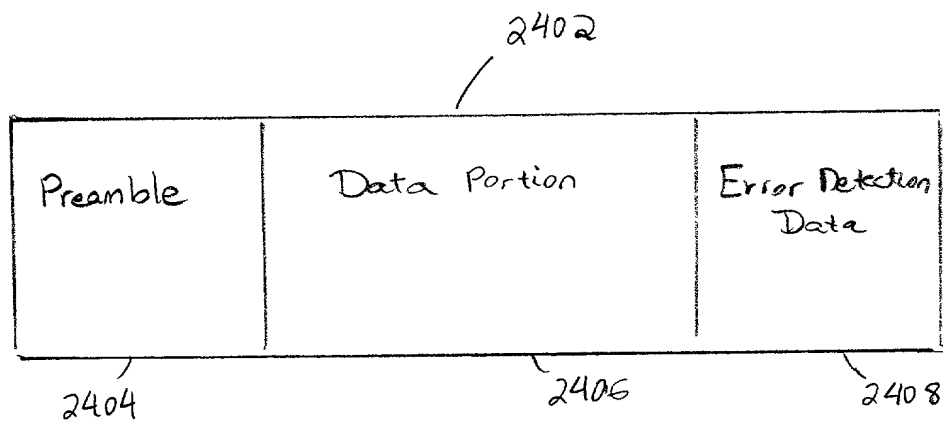


Fig. 24

2025 RELEASE UNDER E.O. 14176

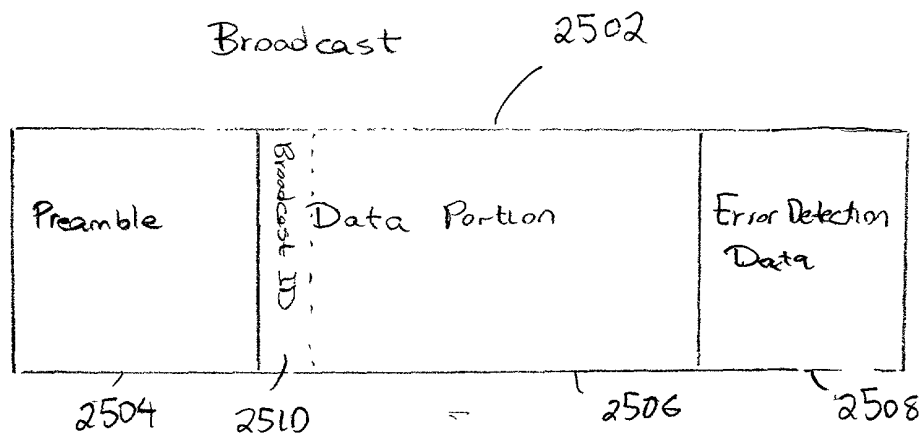


Fig. 25

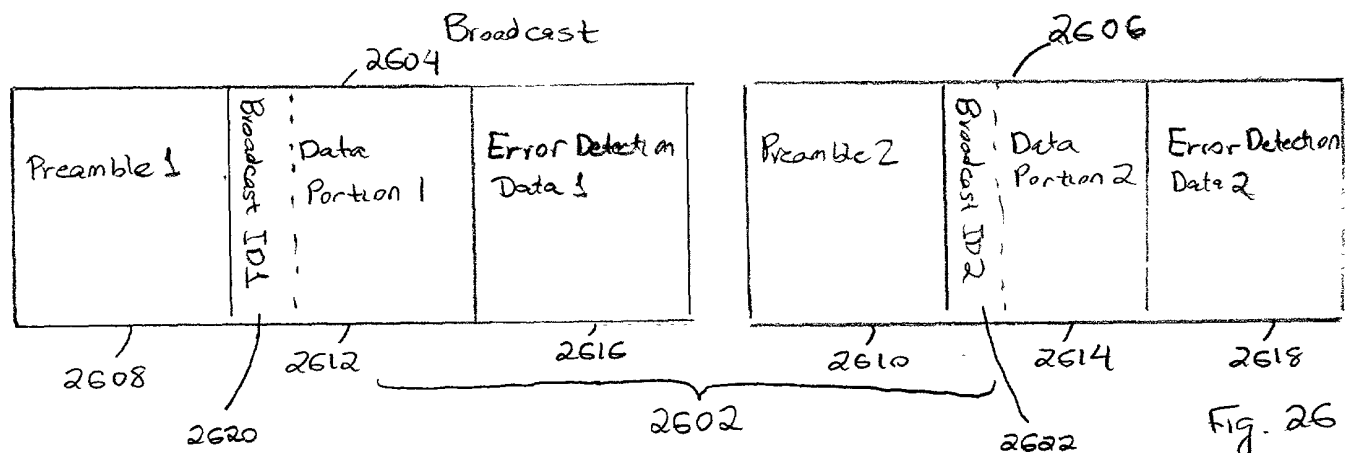


Fig. 26

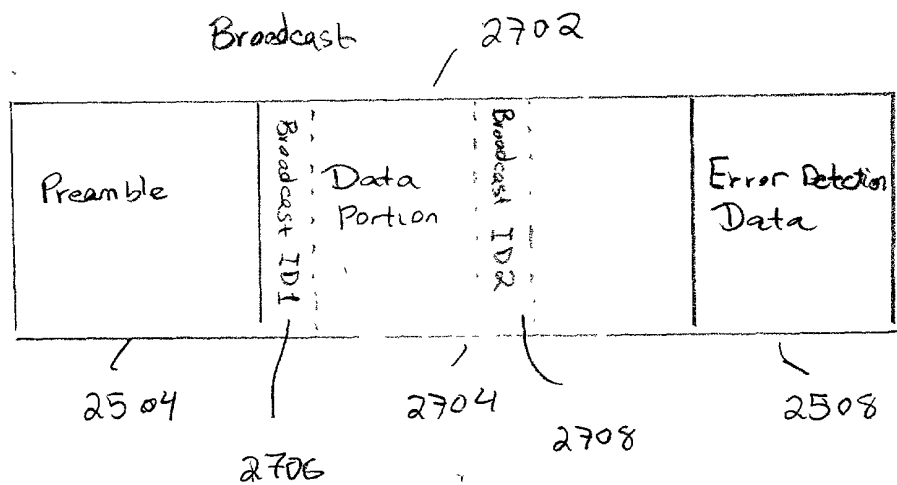


Fig. 27

Battery Conservation

2800

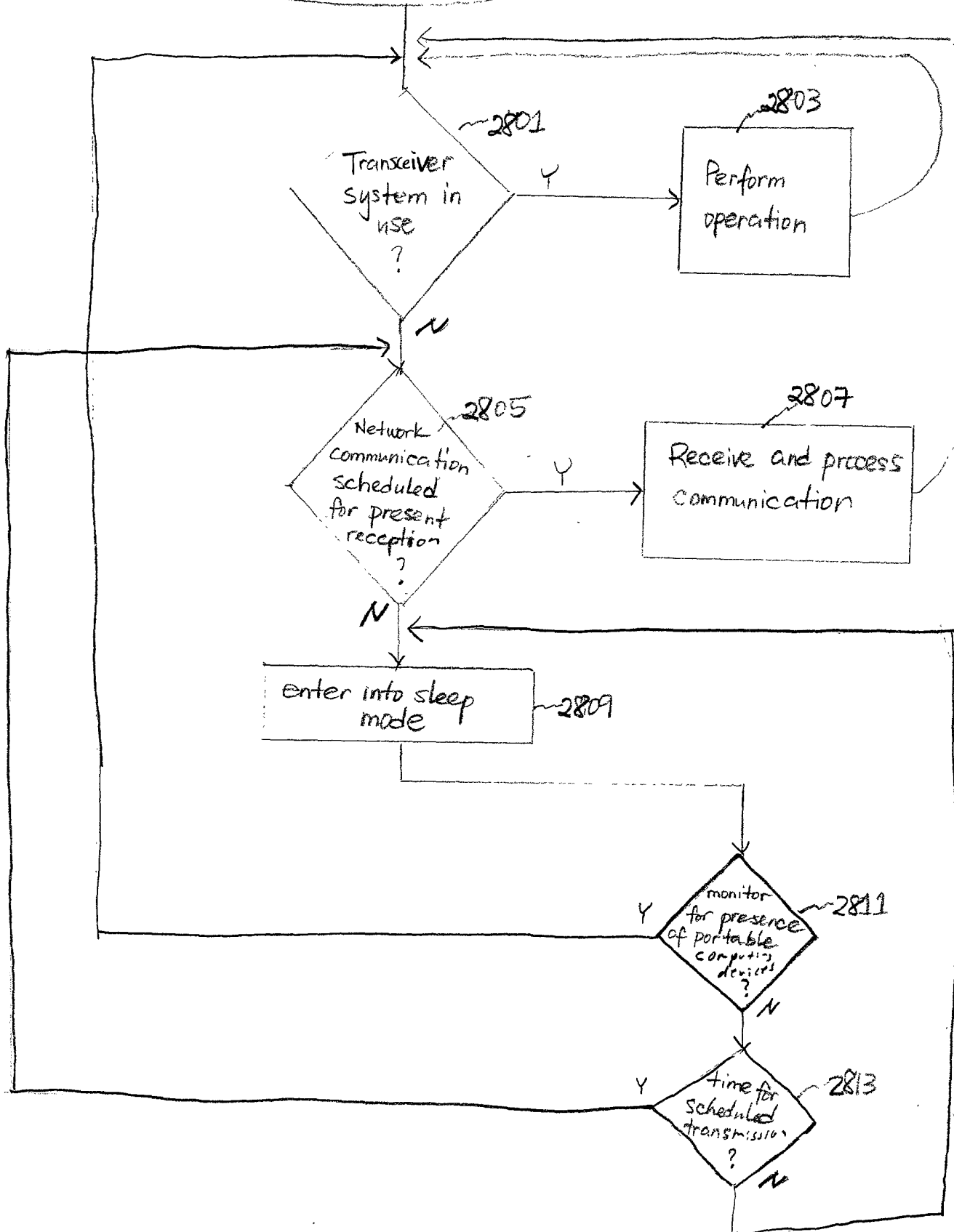


Fig. 28

103

2901

**Modo H/W
Baseband Processor
and
Flex Receiver**

2921

**3.6V
Lithium
Batteries**

**Power
Supply/
Regulation**

**Infrared
Transmitter,
Receivers,
and other UI**

2909

2902

2906

2905

Fig. 29

02674000

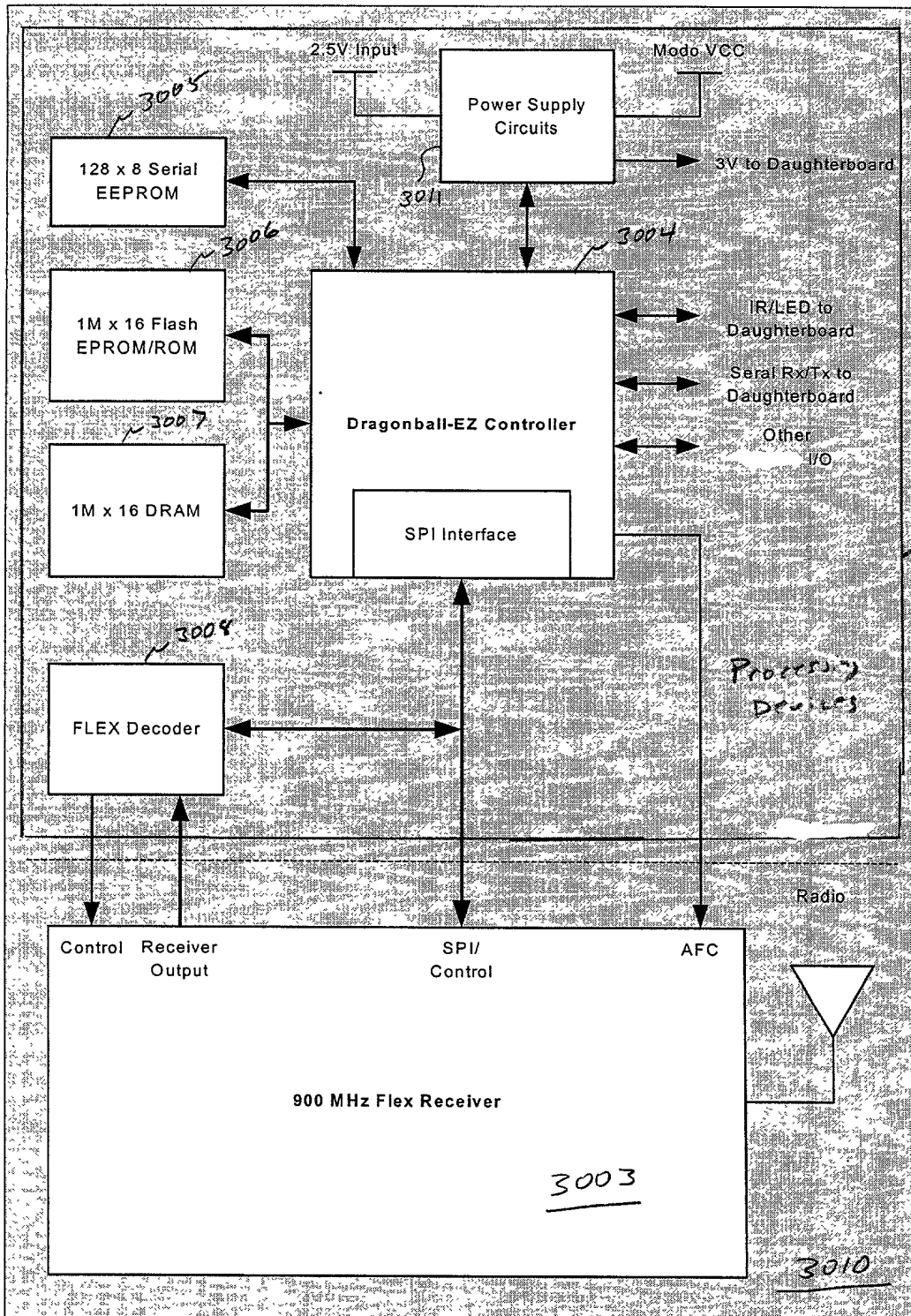
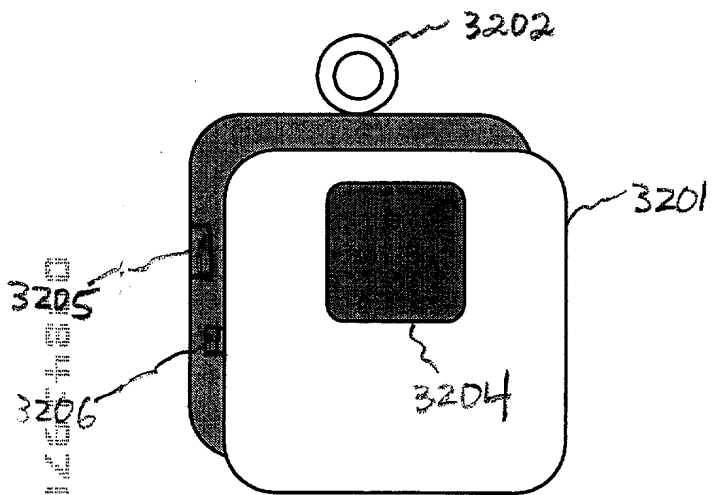


Fig. 30

Figure 2 is a block diagram of the IR receiver and transmitter system. The central component is the **Interface Logic** (3118). It receives **RS-232 from Modo**, **Control and I/O from Modo**, and **3V from Modo**. It also receives **LED Control from Modo** and provides **2.5V to Modo**. The **Interface Logic** is connected to several other components:

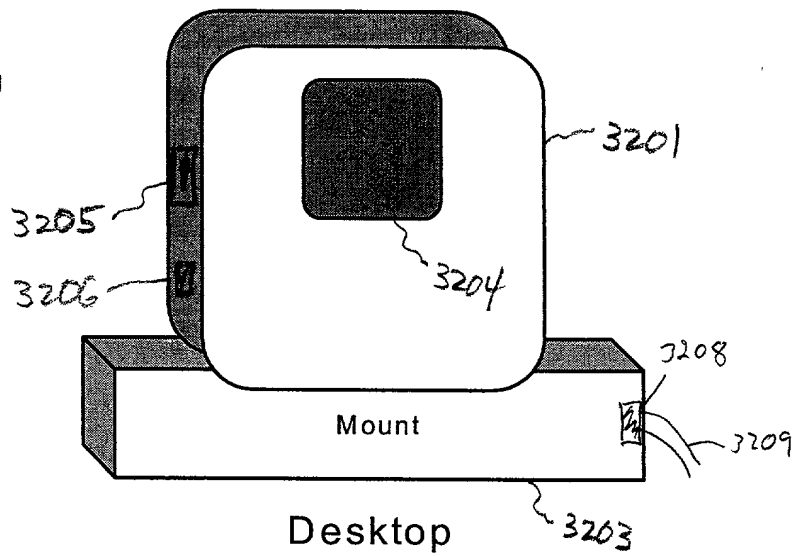
- RS-232 Internal Interface (CMOS)** (3117): Connected via a bidirectional **Serial Data** line.
- IrDA Transceiver** (3113): Connected via a bidirectional **Serial Data** line and a **Power Control** line.
- High Power IR Transmitters** (3119): Connected via a **Serial Data** line.
- 38 kHz ASK Receiver** (3115): Connected via a bidirectional **Serial Data** line and an **Enable** line.
- Status LED** (3116): Controlled by the **LED Control from Modo** signal.
- Switching Regulator** (3112): Receives **3.6V from lithium battery** and provides **2.5V to Modo**.

Fig. 31



Wall Mount

Fig. 32A



Desktop

Fig. 32B

FIG. 33A

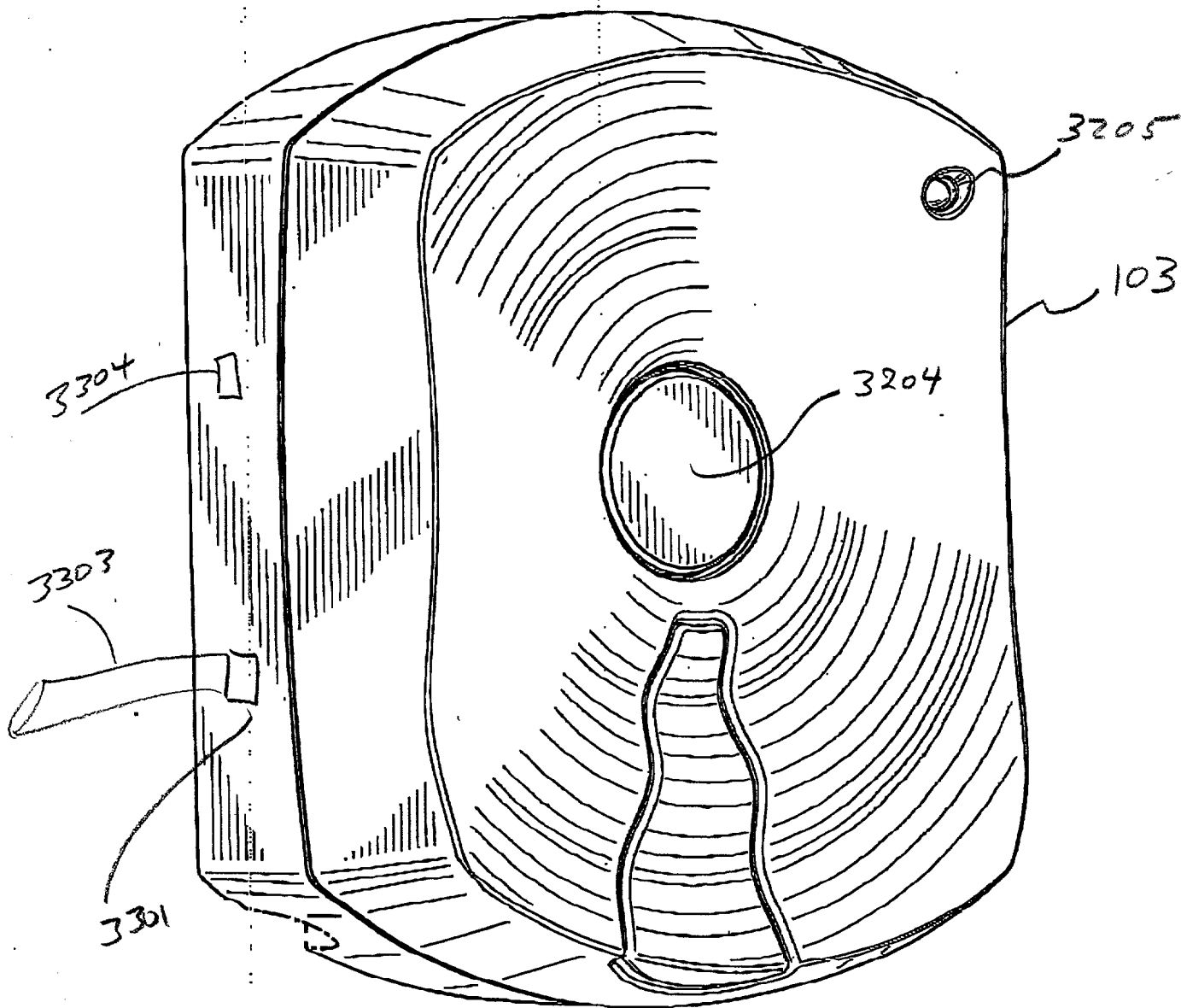


Fig. 33A

LOOKS & TIPS

3305



GREAT FACES

COOL STEEL CREAMY NUDE OPAL SHINE

"It's not about rules. It's about what works for you."



BRIDAL



BLACK TIE



AFRICAN AMERICAN



LATINA



ASIAN

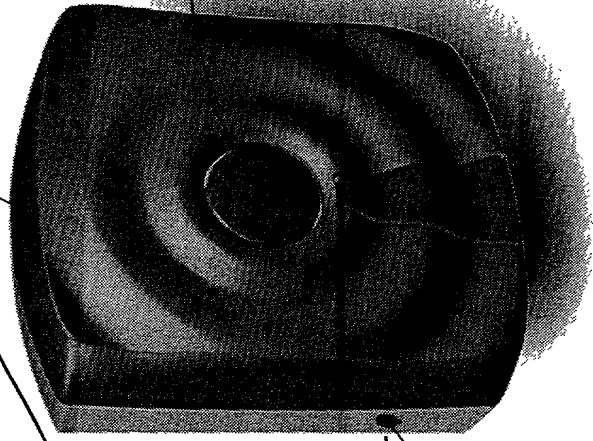


TEEN



AGELESS BEAUTY

103



3204

BUY SOME TODAY

3302

3306

3307

F24 33B

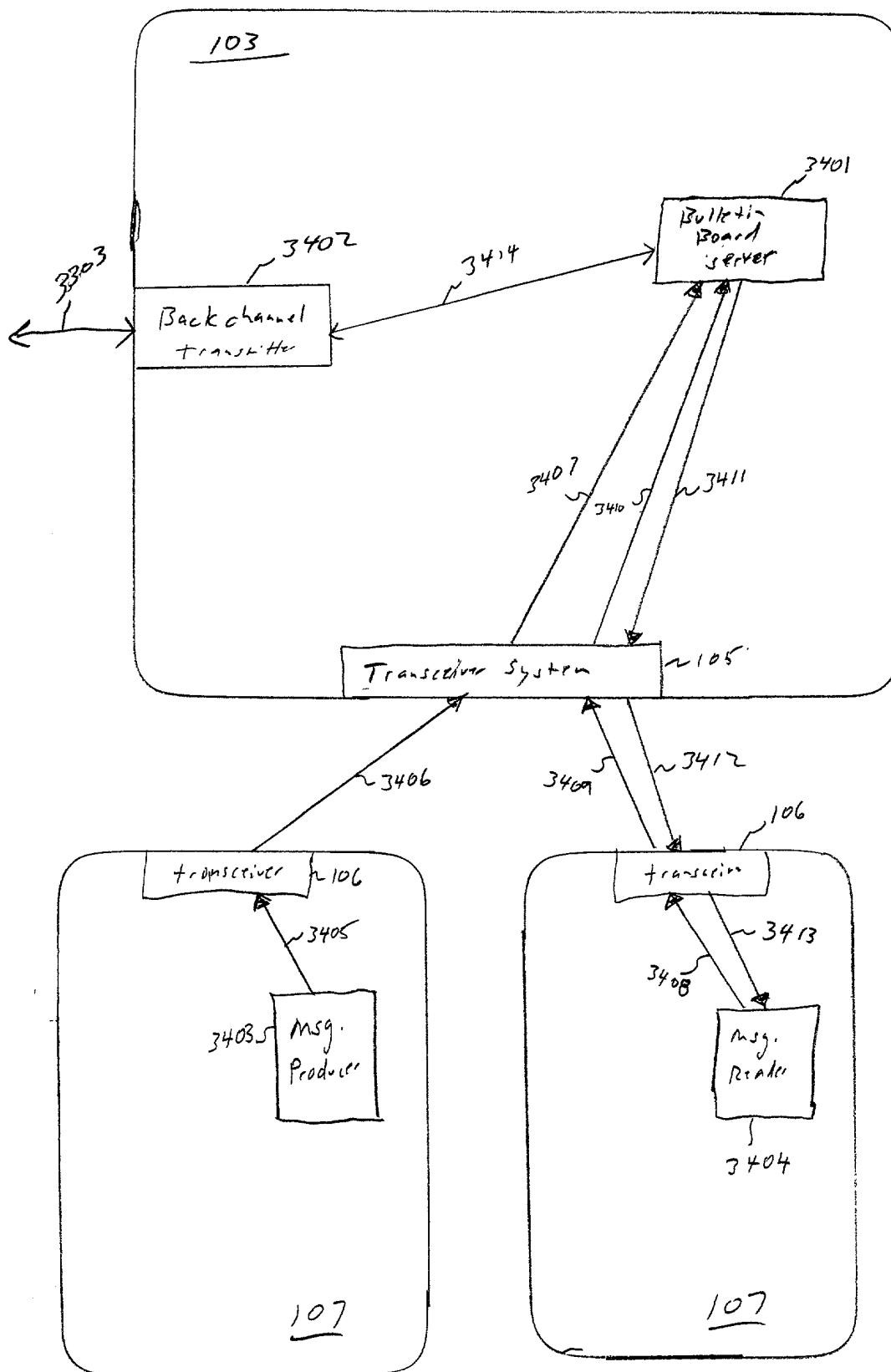


FIG. 34

3500

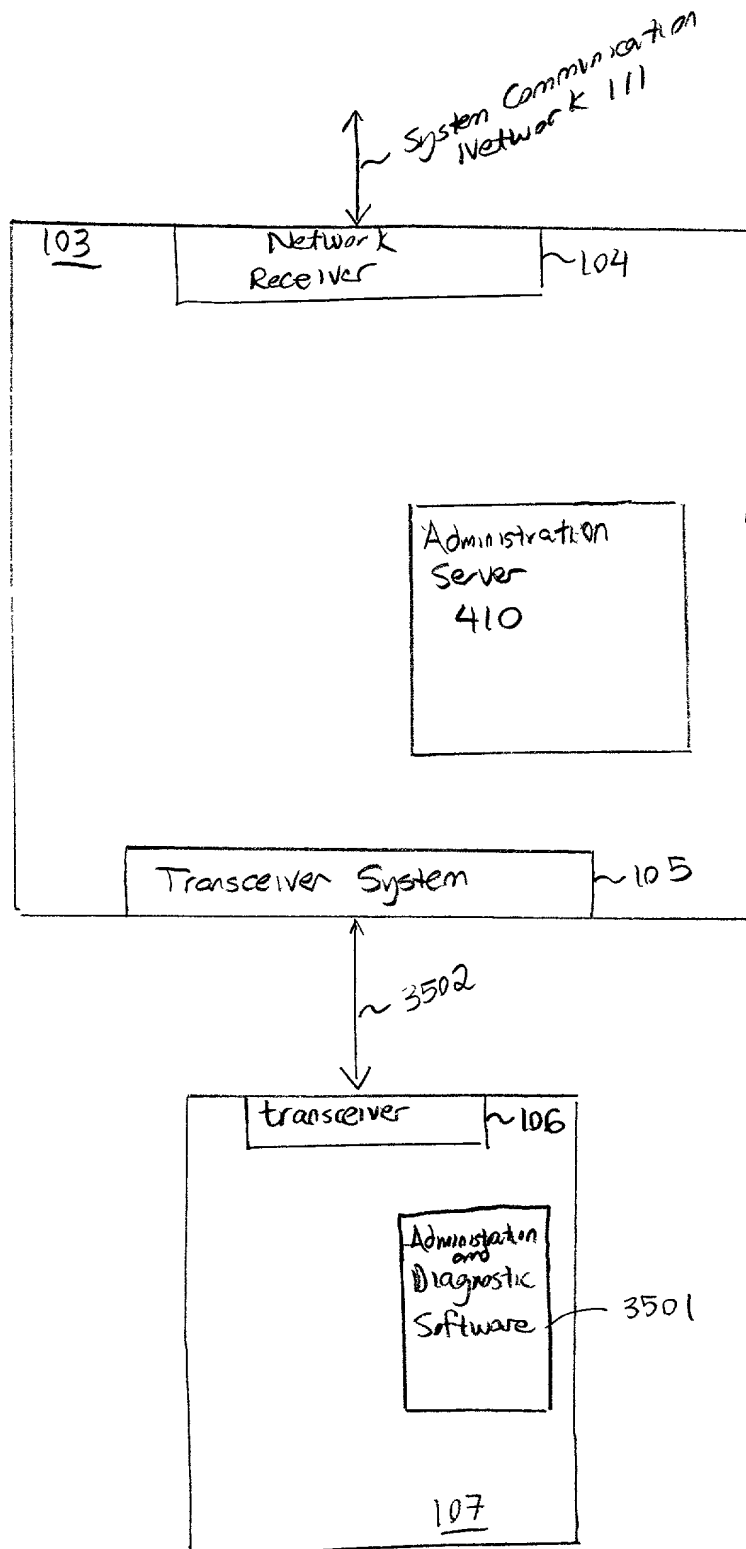


Fig. 35